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single clock design are mapped down to the gate level. With modern computer-aided design (CAD) systems, this process is completely mechanical. The examples illustrate how a CAD system takes advantage of the structure of the control function, including the presence of don't-care terms. Mapping the ALU

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Control Function to Gates

## **Appendix D Mapping Control to Hardware**

Computer Organization and Design, Fourth Edition, has been updated with new exercises and improvements throughout suggested by instructors teaching from the book. It covers the revolutionary change from sequential to parallel computing, with a chapter on parallelism and sections in every chapter highlighting parallel hardware and software topics.

## **Computer Organization and Design: The Hardware/Software ...**

The Fifth Edition of Computer Architecture focuses on this dramatic shift, exploring the ways in which software and technology in the "cloud" are accessed by cell phones, tablets, laptops, and other mobile computing devices. Each chapter includes two real-world examples, one mobile and one

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datacenter, to illustrate this revolutionary change.

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